			DOC # SVT-DIPS				
		LABORATORY MANAGEMENT SYSTEM	REVISION # A				
	FlexLabinfo.org		SUPERSEDE None				
	FLEX LAB INFO EMCFLEXBLOG	SUPPLY VOLTAGE TRANSIENT TEST INPUT VOLTAGE DROPOUTS,	RELEASE DATE 2024-01-12				
	Tel: 416-385-9894	DIPS AND SWITCHED LOGIC	DOC TYPE WORK INSTRUCTION				
1.	SCOPE						
1.1.	This test procedure describes a method for exposing electronic modules to momentary dips, dropouts and switching transients (input and output lines) of input voltage to the module.						
1.2.	Dip is a momentary reduction of supply voltage. Drop Out is a momentary loss of supply voltage at the inputs of an electronic module. Dropouts may occur repetitively or singly. They may occur for duration of 10 microsecond up to 1.5 seconds, as defined in the test plan. Mechanical Switching Transient (Logic Inputs) is a transient produced by the non ideal opening or closure of a mechanical switch (switch bounce) producing a noisy level transition. These switching transients are simulated by a pulse source and may consist of one or a series of pulses following the nominal switching event. Switched Logic Input is an input to the DUT which responds to a digital signal source from either a regulated voltage or from system ground.						
2.	VOLTAGE DROPOUTS TEST						
2.1.	Voltage Dropouts can occur in the vehicle during the operation of switches and electrical loads or it can be caused by loose connections. The test is designed to exceed realistic vehicle conditions in order to trigger potential susceptibilities in analog and digital circuits, which may not be apparent under normal operating conditions, particularly digital logic reset and memory functions. All electronic devices designed for the vehicle environment should be exposed to this procedure to test their performance when subjected to momentary fluctuations of voltage at their inputs.						
3.	VOLTAGE DIPS TEST						
3.1.	This test simulates the voltage dip that can occur when a heavy load such as the A/C compressor or radiator fan switches on.						
4.	SWITCHED LOGIC TRANS	IENTS TEST					
4.1.	This test is designed to simulate n	nechanical s itch bounce.					
5.	TEST EQUIPMENT						
5.1.	Drop out test equipment: >1 A, 1 +/- 0.5 μs rise time, EM TEST / AMETEK VDS 200 or equivalent. Oscilloscope: BW >10 MHz, Tektronix or equivalent. Power Supply: 13.5 Volts +/- 0.2 Volts, SAE J1113/1, paragraph 6.6 HP 6033 or 6038 or equivalent. Pulse Generator: Pulse width > 1 μs, HP 8112A or equivalent Switched Logic test equipment: >1 A, 1 +/- 0.5 μs rise time, EM TEST / AMETEK VDS 200 or equivalent. Voltage Probe: BW >10 MHz, Tektronix probe or equivalent. Programmable Controller GPIB/USB: for control of instruments in automated tests. Data Acquisition Equipment GPIB/USB: for measurement and control.						
6.	TEST METHOD						
6.1.	A Voltage Drop Generator is placed in series with individual input leads of the DUT to cause a momentary dropout of voltage at supply and switched logic inputs. DUT inputs, which receive an unregulated voltage from the vehicle electrical system, are exposed to voltage dips ranging from 13.5 to 0 V dc. Switched logic inputs are exposed to momentary closures to either system ground or to regulated voltage.						
6.2.	Functional Performance Status Classification         Use categories per SAE J1113-1. or as defined by automotive OEM EMC spec.         The test plan includes:         • classification of DUT functions         • performance region requirements         • inputs and outputs to be tested         • system voltage levels to be applied         • functions to be monitored         • the number of samples required         • DUT identifier and development level (ED/DV/PV)         • PCB(s) identifier						
6.3.	DUT Configuration The DUT is set up to perform the functions specified in the test plan. All inputs to be tested may be connected to a number of switches at once. However, the dropouts are normally done one input at a time. In some cases, combinations of inputs are exposed to various dip levels to simulate known vehicle conditions.						
6.4.	Equipment Configuration A Voltage Drop Generator is place	ed in series with each DUT input designated in the test plan to allow momentary dropo	uts, dips of voltage, and switched logic.				

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	the desired pulse at the minimum pulse duration (normally 10 $\mu$ s). The pulse shinder test. Therefore, measurements of pulse duration and level are made by temp			
<ul> <li>The DUT can be instal actuators are directly or installation and no othe</li> <li>The component 12 V p</li> <li>The cross-section of th</li> <li>For other lines, a wiring</li> <li>In the case of sensor or equivalent resistance.</li> <li>Operate the DUT to pe</li> <li>Apply 5 repetitive drop</li> <li>Increment the pulse du</li> <li>Note that the tolerance</li> <li>When an effect on req threshold between pulse</li> <li>Apply 5 repetitive drop an effects.</li> <li>Complete the above statistic complete the above statistic complete the the the duration of the drop of the supply voltage mu</li> <li>The supply voltage mu</li> <li>The component operation of the drop of the duration of</li></ul>	<ul> <li>Complete the above steps for each terminal of the DUT to be tested.</li> <li>At this point, connect all of the battery lines tested in the above paragraphs together and repeat the same procedure as outlined above for this configur.</li> <li>Note that the Test Parameters may differ based on EM specification that outlines what dropout voltage and intervals to use.</li> <li>The supply voltage must drop out from 11 V to 0 V and return to 11 V.</li> <li>The duration of the drop out increases from 100 µs to 2 s in increments as shown in TBL 6.1 - A.</li> <li>Drop outs are set by open circuit (≥1 kΩ load) with fall time and rise time less than 10 µs each.</li> <li>The component operation must be monitored during the test and the interval time between dropouts is sufficient to verify normal component operation.</li> <li>Minimum number of drop outs for each duration is10.</li> <li>Time between drop outs is minimum 5 s or response time of the component, whichever is greater.</li> </ul>			
installation and no othe The component 12 V p The cross-section of th For other lines, a wirin In the case of sensor of equivalent resistance. Set up the test equipm 1.5 V in steps of 0.5 V. The pulse is applied for Apply 5 dips to the terr Increase the voltage d Record all effects with Repeat the above step Monitor the performanci circuits. At this point, connect at Note that the Test Parameters may A dip is from 11 V to th The dip voltages are: 5 Dips to each voltage le Voltage shall be applie The component operation	er ground connection is authorized. ower (and its associated ground) lines submitted to the test must have a maximur e wiring must be representative. g harness of a maximum length of 2000 mm should be preferably used (using the utputs connected to the 12 V through a pull up resistor, the concerned outputs mu Its value has to be provided in the technical specification and is specified in the test ent and Voltage Dip Generator to achieve a voltage dip from 13.5 V to V (minimum r a minimum duration of 100 $\mu$ s. ninal(s) under test with 3 seconds minimum between dips, while monitoring DUT p p amount by increments of 0.5 V ±5% applying 5 repetitive dips for each incremer	n length of 500 mm. real wiring harness is allowed). Ist be connected to the generator through the st plan. n) where V (minimum) is equal to 5.5 V decreasing to performance. Intal dip level. Ins related to memory storage, digital logic and reset the procedure as outlined above for this configuration. Ister rise time less than 50 s each (during the test). be sufficient to verify normal component operation.		
6.7. Switched Logic Inputs Test		··· ·		

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for example c A M M F Low frequenc Generator. In F M	<ul> <li>Low frequency (&lt;10 Hz) logic inputs, which respond to a switched closure to system ground, are to be tested using a Voltage Drop Generator (see FIG 6.1 - B for example configuration).</li> <li>Apply single switch closures to ground of 0.01, 0.1, 1, 10 and 100 milliseconds duration.</li> <li>Multiple ground outs (pulse trains) of 10 percent duty cycle or less may also be used at each increment.</li> <li>Monitor the performance of the DUT function associated with the logic input being tested.</li> <li>Record effective "switch bounce" performance or any other effect to functional performance.</li> <li>Low frequency switched logic inputs which respond to a switched closure to a regulated voltage source are to be tested using a drop out type Voltage Drop Generator.</li> <li>Insert the switch in series with the terminal to be tested.</li> <li>Pulse the switch circuit to achieve momentary digital high signals at the duration of 0.01, 0.1, 1, 10 and 100 milliseconds.</li> <li>Monitor for effects.</li> <li>For this test, lines must be tested individually.</li> </ul>						
Figure (6.1) FIG	i.1 - A	FIG 6.1 - B		TBL 6.1 -A			
CONTROL	We keep 5 200 mm GPI8 COAX WIRING OBCILLOSCOPE COAL CO	SWITCHED LOGIC SWITCHED LOGIC INPUT (IGH) SWITCH TYPE OPEN TO LOGIC HIGH SWITCH TYPE OPEN TO LOGIC LOW (GROUND) SWITCH TYPE OPEN TO LOGIC LOW (GROUND) SWITCH TYPE OPEN TO LOGIC LOW (GROUND) DUT ZZZZZZZZZZZZZZZZZZZZZZZZZZZZZZZZZZZ	INC STAF 10 µ 100 µ 1 m 10 m 10 m	RT         STOP           s         100 μs           ιs         1 ms           s         10 ms           is         10 ms	ZES TABLE INCREMENT 10 μs 100 μs 1 ms 10 ms 100 ms		
		DUT. This is especially useful for functions, which are not equipment may be interfaced to a computer to allow automatic			s operation, memory		
		REVISION CHANGES					

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## **END OF DOCUMENT**

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